

REMARKS

Prior to entry of this paper, Claims 1-20 were pending. Claims 1-20 were rejected. In this paper, several claims are amended; Claims 4, 5, and 13 are cancelled; and Claims 21-28 are added. Claims 1-3, 6-12, and 14-28 are currently pending. No new matter is added by way of this amendment. For at least the following reasons, Applicants respectfully submit that each of the presently pending claims is in condition for allowance.

Claims 1-3, 6-12, and 14-28

Claims 1-20 were rejected under 35 U.S.C. §102(b) as being anticipated by Bult (U.S. Patent No. 6,100,836).

Claim 1 is respectfully submitted to be allowable at least because Bult fails to disclose “a folding amplifier array” (as recited in Applicants’ Claim 1), in conjunction with the other limitations of Applicants’ Claim 1.

Claims 2, 3, and 6-10 are respectfully submitted to be allowable at least because they depend from Claim 1.

Claims 11, 20, and 21 are respectfully submitted to be allowable at least for similar reasons to those stated above with regard to Claim 1. Claims 12, 14, and 16-19 are respectfully submitted to be allowable at least because they depend from Claim 11. Claim 22 is respectfully submitted to be allowable at least because it depends from Claim 21.

The rejection to Claim 15 is respectfully traversed at least because Bult fails to disclose, “the first current source circuit” in conjunction with the limitation, “each of the plurality of load circuits includes a load current source”. In the last paragraph of Claim 4, discussing Claim 11, the Office Action states that the load circuits of Bult are each of 62a, 62b of Bult. Under this theory, it is inconsistent to state that the load circuits includes current sources as recited in Applicants’ Claim 15. In this paragraph, the Office Action also states that current sources of Bult are each 54 of Bult. With regard to Claim 15, the Office Action states that each of the load circuits includes a current source (54, 56). However, Claim 15 recites the current source circuits as separate elements from the plurality of loads circuits. However, the current source 54 cannot simultaneously act as the recited

current source and the part of the recited load, and fall within the scope of the Claim 15, since Claim 15 recites that the circuit include both a load and a current source as separate elements.

For at least these reasons, it is respectfully submitted that Claim 15 is in condition for allowance.

Claim 23 is respectfully submitted to be allowable at least because Bult fails to disclose, “a first current source circuit that is arranged to provide a first local current at an output of the first amplifier circuit such that at least a portion of the first output current is prevented from being carried on the bus; and a load circuit that is coupled to the bus, wherein the load circuit is configured to provide an output voltage such that the output voltage is substantially equal to the multiplicative product of: the bus current, and an impedance of the load circuit”, as recited in Applicants’ Claim 23.

Claim 24 is respectfully submitted to be allowable at least because Bult fails to disclose, “a first current source circuit that is arranged to provide a first local current at an output of the first amplifier circuit such that at least a portion of the first output current is prevented from being carried on the bus; and a load circuit that is coupled to the bus, wherein the load circuit does not include averaging impedances”, as recited in Applicants’ Claim 24.

Claim 25 is respectfully submitted to be allowable at least because Bult fails to disclose, “a first current source circuit that is arranged to provide a first local current at an output of the first amplifier circuit such that at least a portion of the first output current is prevented from being carried on the bus; and a load circuit that is coupled to the bus, wherein the load circuit is coupled to a supply voltage”, as recited in Applicants’ Claim 25.

Claim 26 is respectfully submitted to be allowable at least because Bult fails to disclose, “a first current source circuit having at least an output, wherein the output of the first current source circuit is connected to the bus, and wherein the first current source circuit is arranged to provide a first local current at an output of the first current source circuit such that at least a portion of the first output current is prevented from being carried on the bus; and a second amplifier circuit of the plurality of circuits, wherein the second amplifier circuit has at least an output that is connected to the bus, and wherein the second amplifier circuit is configured to provide a second output current at the output of the second amplifier circuit”, as recited in Applicants’ Claim 26.

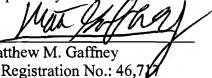
Claim 27 is respectfully submitted to be allowable at least because it depends from Claim 26. Claim 28 is respectfully submitted to be allowable at least because Bult fails to disclose a circuit having all of the elements recited in Applicants' Claim 28.

CONCLUSION

It is respectfully submitted that each of the presently pending claims (Claims 1-3, 6-12, and 14-28) are in condition for allowance and notification to that effect is requested. Examiner is invited to contact the Applicants' representative at the below-listed telephone number if it is believed that the prosecution of this application may be assisted thereby. Although only certain arguments regarding patentability are set forth herein, there may be other arguments and reasons why the claimed invention is patentable. Applicant reserves the right to raise these arguments in the future.

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Respectfully submitted,

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